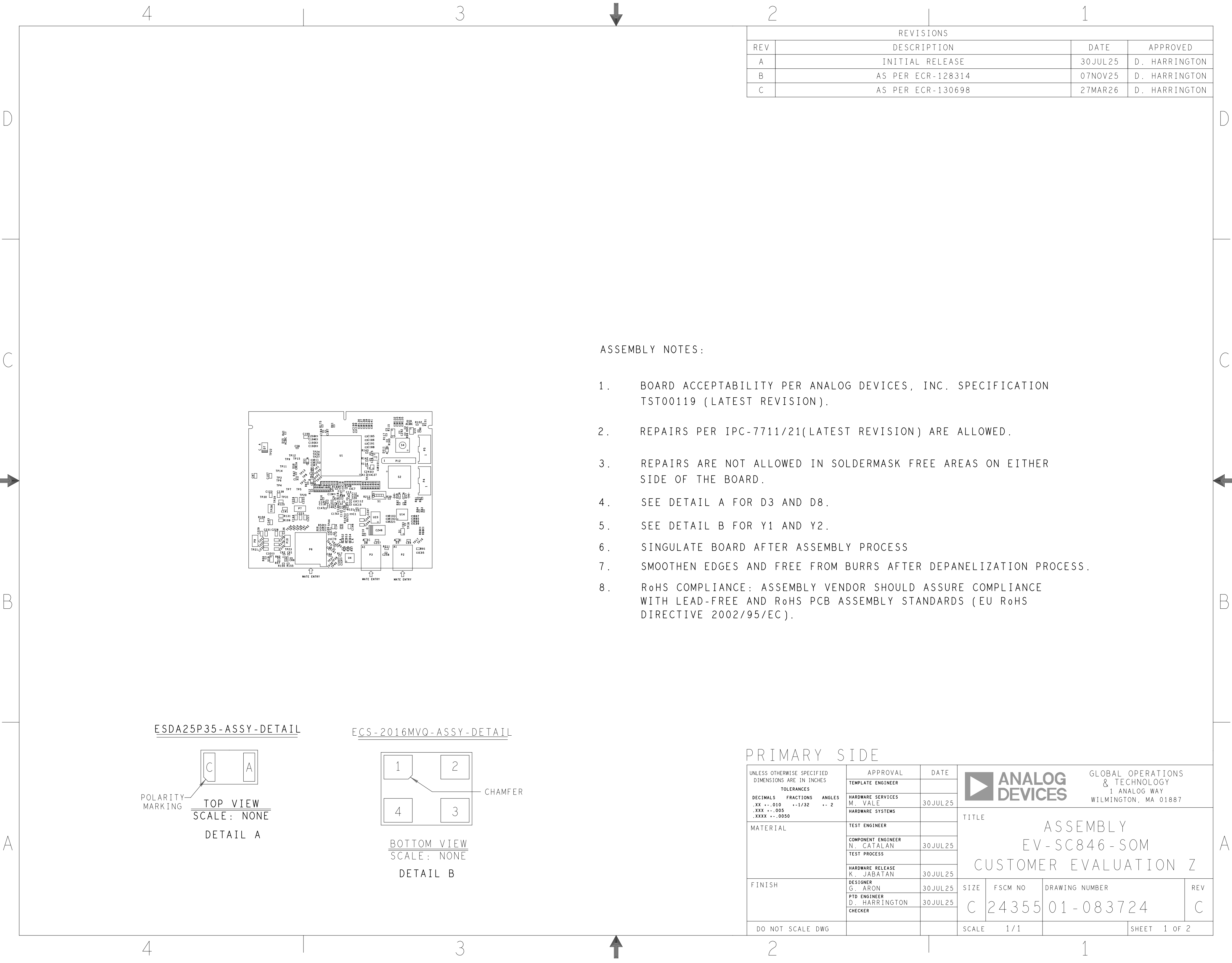




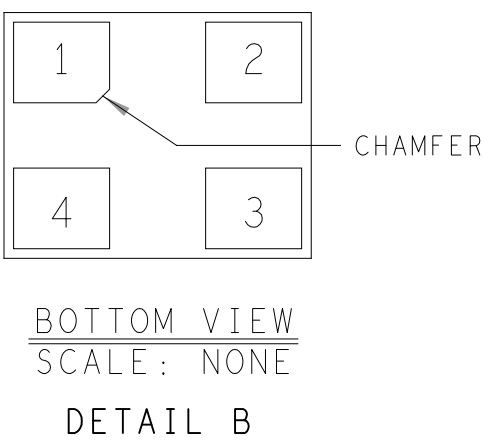
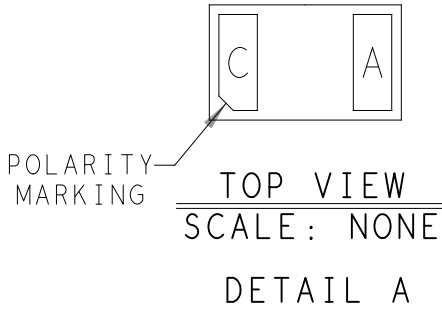
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	30JUL25	D. HARRINGTON
B	AS PER ECR-128314	07NOV25	D. HARRINGTON
C	AS PER ECR-130698	27MAR26	D. HARRINGTON

ASSEMBLY NOTES:

- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
- SEE DETAIL A FOR D3 AND D8.
- SEE DETAIL B FOR Y1 AND Y2.
- SINGULATE BOARD AFTER ASSEMBLY PROCESS
- SMOOTHEN EDGES AND FREE FROM BURRS AFTER DEPANELIZATION PROCESS.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).

ESDA25P35-ASSY-DETAIL

ECS-2016MVQ-ASSY-DETAIL



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050	APPROVAL	DATE	GLOBAL OPERATIONS & TECHNOLOGY 1 ANALOG WAY WILMINGTON, MA 01887 TITLE ASSEMBLY EV-SC846-SOM CUSTOMER EVALUATION Z			
	TEMPLATE ENGINEER					
	HARDWARE SERVICES M. VALE	30JUL25				
	HARDWARE SYSTEMS					
MATERIAL	TEST ENGINEER		SIZE FSCM NO DRAWING NUMBER REV C 24355 01-083724 C			
	COMPONENT ENGINEER N. CATALAN	30JUL25				
	TEST PROCESS					
	HARDWARE RELEASE K. JABATAN	30JUL25				
FINISH	DESIGNER G. ARON	30JUL25	SCALE 1/1 SHEET 1 OF 2			
	PTD ENGINEER D. HARRINGTON	30JUL25				
	CHECKER					
	DO NOT SCALE DWG					

